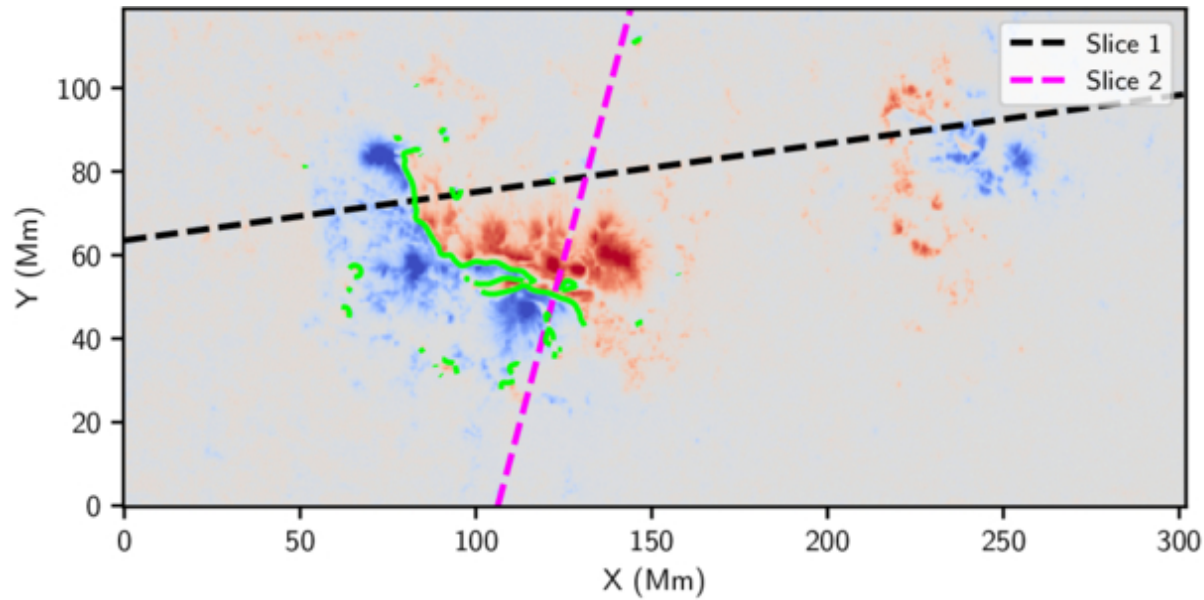
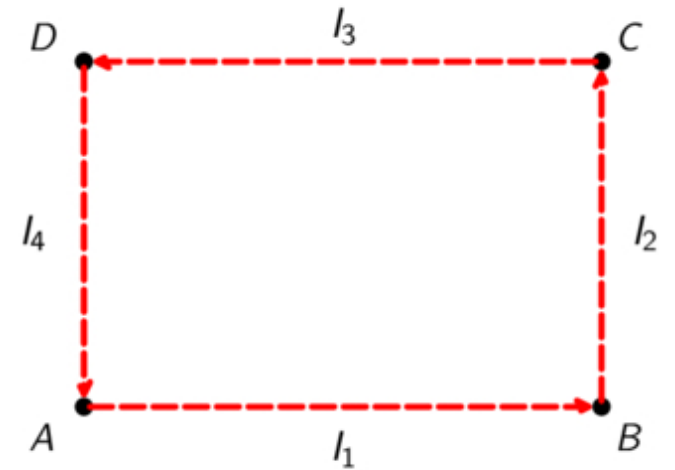
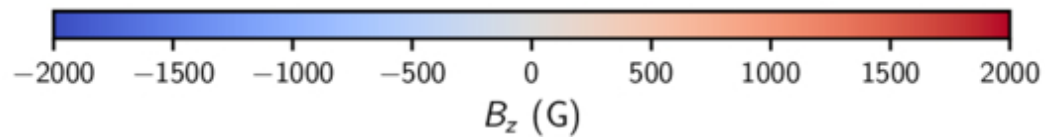


Figure 7. from
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<https://dx.doi.org/10.3847/1538-4357/ae6a8b>
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(a) Vertical slices perpendicular to PIL



(b) Contour loop schematic